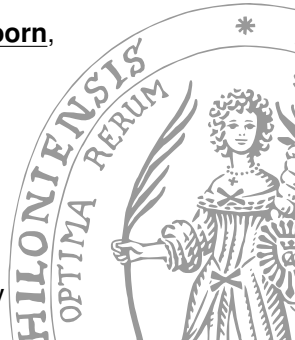


Performance Simulation of Runtime Reconfigurable Component-Based Software Architectures

Robert von Massow, André van Hoorn,
and Wilhelm Hasselbring

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Christian Albrechts University of Kiel

Sept. 14, 2011 @ ECSA 2011, Essen, Germany



Context of this Work

Online Capacity Management for Increased Resource Efficiency

Introduction



The screenshot shows the CEWE website homepage. The header includes the CEWE logo and navigation links for 'Produkte', 'Unternehmen', 'Karriere', 'Presse/News', 'Investor Relations', 'Kontakt', and 'Nachrichten'. A main banner features a 'Disney Shop' with a list of products: Disney's Cinderella, Disney Park, Disney's Mickey Mouse, Disney's Star Wars, Disney's Pixar, Disney's Marvel, Disney's Pixar Cars, and Disney's Pixar Cars. Below the banner are several product categories: 'CEWE COLOR in Best Individualität', 'Karten', 'Investor Relations', 'Presse/News', 'Mensch & Natur', and 'Produktentwicklung'. The footer contains contact information and a 'CEWE COLOR' logo.



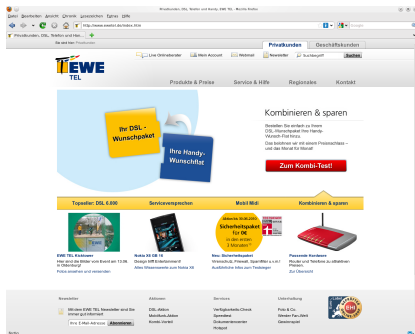
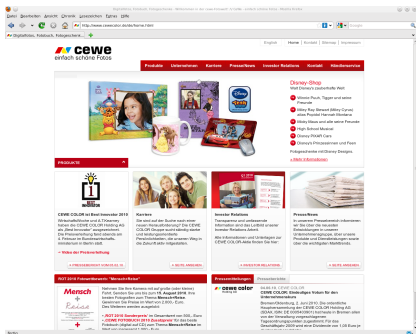
The screenshot shows the CEWE TEL website. The header includes the CEWE TEL logo and navigation links for 'Produkte & Preise', 'Service & Hilfe', 'Registrieren', and 'Kontakt'. A main banner features a 'Kombinieren & sparen' offer with a 'Zum Kombi-Test' button. Below the banner are several product categories: 'Topper: DSL 4.800', 'Serviceansprechen', 'Mobil M&M', and 'Kombinieren & sparen'. The footer contains contact information and a 'CEWE' logo.

- Business-critical software systems

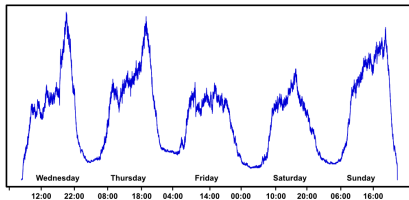
Context of this Work

Online Capacity Management for Increased Resource Efficiency

Introduction



- Business-critical software systems
- Quality of service (performance, availability, ...)

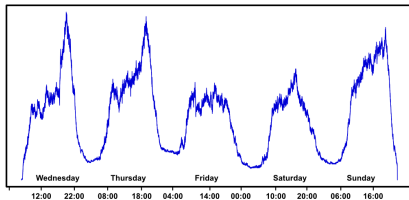


- Business-critical software systems
- Quality of service (**performance**, availability, ...)
- Varying workloads + static capacity management

Context of this Work

Online Capacity Management for Increased Resource Efficiency

Introduction



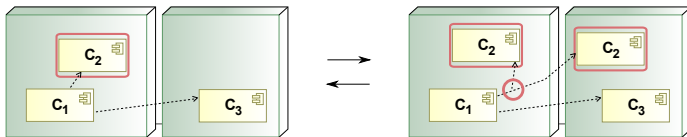
Problem: Overprovisioning — unnecessarily high operating costs

Underutilized resources during medium/low workload periods

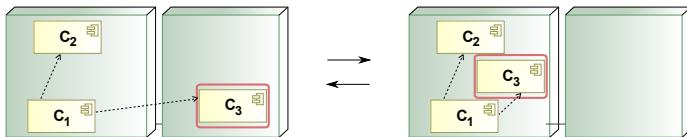
Goal: Increase resource efficiency while meeting SLAs

▷ **SLAStic** [vHRGH09, vH11]: Online capacity management employing runtime reconfiguration

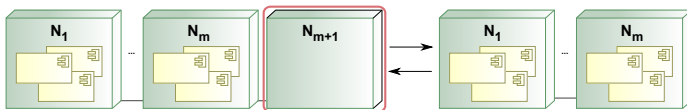
1 (De-)Replication of Software Components



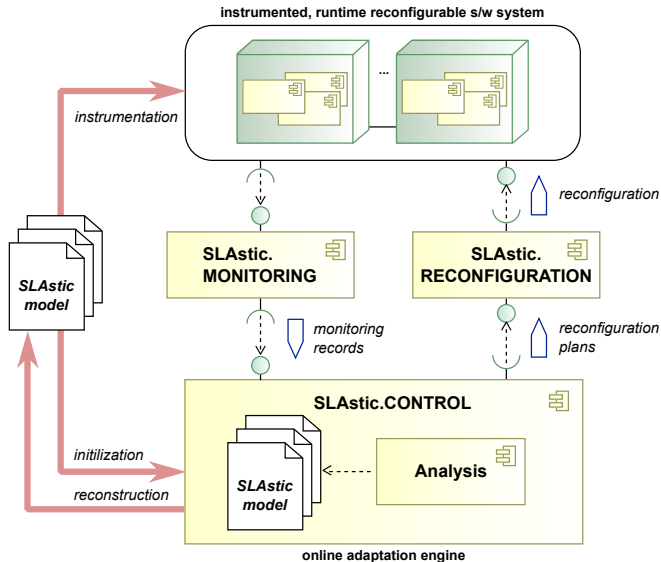
2 Migration of Software Components



3 (De-)Allocation of Execution Containers



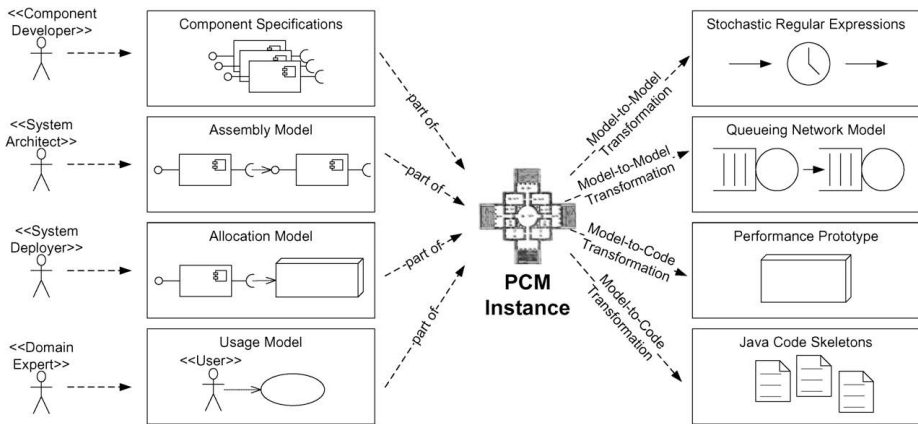
- 1 Introduction
- 2 SLAStic — Framework & PCM-Specific Reconfiguration
- 3 SLAStic.SIM — Simulator Architecture & Framework Integration
- 4 Evaluation
- 5 Conclusions



Palladio Component Model (PCM)

[BKR09, RBH⁺07]

SLAStic — Framework & PCM-Specific Reconfiguration



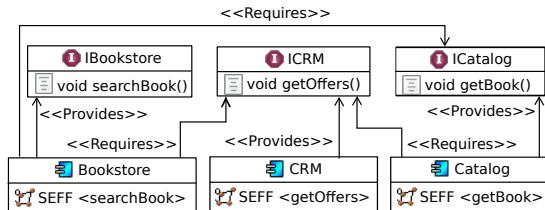
[BKR09]

1 PCM Repository Model

- Components & Interfaces
- RDSEFFS

2 PCM System Model, Resource Env. & Allocation

3 Scenario searchBook()

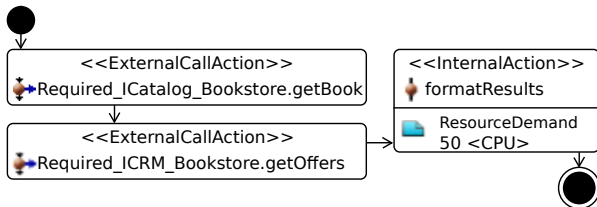


1 PCM Repository Model

- Components & Interfaces
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3 Scenario searchBook()



RDSEFF of `Bookstore.searchBook()`

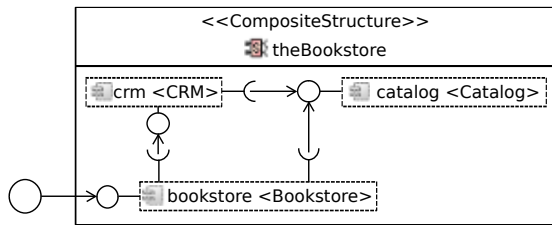
- `Bookstore.searchBook()`: External calls: `getBook()` & `getOffers`; CPU demand: 50
- `Catalog.getBook()`: External call: `getBook()`; CPU demand: 20
- `CRM.getOffers()`: CPU demand: 15

1 PCM Repository Model

- Components & Interfaces
- RDSEFFS

2 PCM System Model, Resource Env. & Allocation

3 Scenario searchBook()



System Model

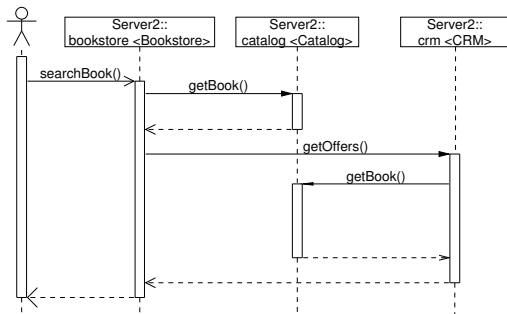
- **Resource Environment:** 2 resource containers (Server1 & Server2)
- **(Initial) Allocation:** 1 allocation ctx. per assembly ctx. on Server2; Server1 empty

1 PCM Repository Model

- Components & Interfaces
- RDSEFFS

2 PCM System Model, Resource Env. & Allocation

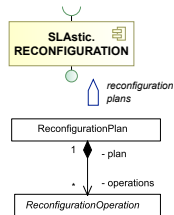
3 Scenario `searchBook()`



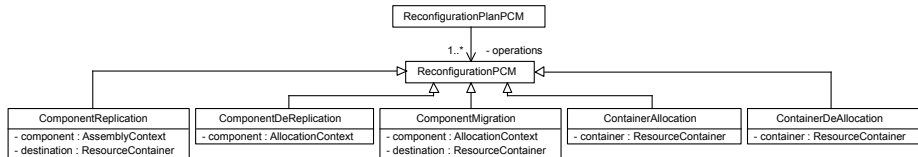
(reconstructed from simulation data)

Architecture Level Operation Signatures (based on SLAStic Meta-Model):

- 1 (De-)Replication of Software Components
 - replicate (component: [AssemblyComponent](#), to: [ExecutionContainer](#))
 - dereplicate (component: [DeploymentComponent](#))
- 2 Migration of Software Components
 - migrate (component: [DeploymentComponent](#), to: [ExecutionContainer](#))
- 3 (De-)Allocation of Execution Containers
 - allocate (containerType: [ExecutionContainerType](#))
 - deallocate (container: [ExecutionContainer](#))



Technology-specific Operation Signatures (here: PCM [BKR09]):



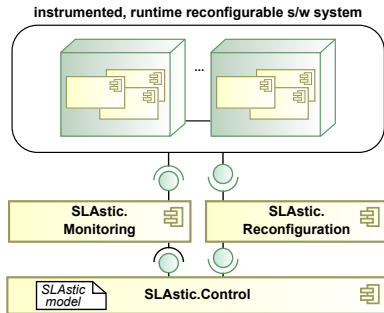
Rationale: Simulation-based . . .

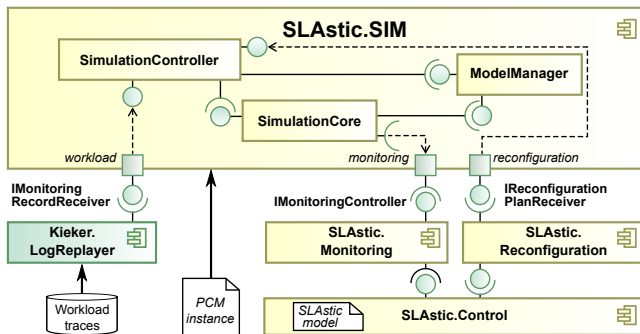
- 1 Study (transient & stationary) effects of reconfigurations (implementation “costly”)
- 2 Online performance prediction (e.g., proactive problem detection, online planning)
- 3 Offline performance prediction (e.g., evaluation of adaptation strategies/techniques)
- 4 Evaluation of overall approach (in addition to lab & case studies)

Our Requirements for a Performance Simulator:

- 1 Architectural style: C-B software systems (assembly, deployment, . . .)
- 2 Workload: Replay traces, support varying workload
- 3 Runtime reconfiguration: Support SLAStic operations
- 4 Measures: Performance (timing, CPU utilization)

- 1 Introduction
- 2 SLAStic — Framework & PCM-Specific Reconfiguration
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- (Initial) transformation of PCM instance into internal representation
- SLAStic.SIM employs Desmo-J¹ [PK05] discrete-event simulation engine
- Kieker used for providing workload traces and for monitoring simulation data
- Probes injected with Google Guice²

¹<http://desmoj.sourceforge.net/> ²<http://code.google.com/p/google-guice/>

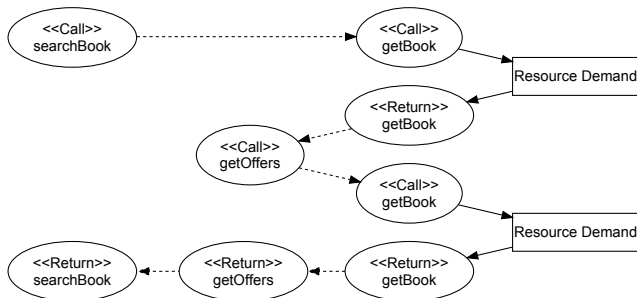
- **Workload**
 - Driven by externally provided workload events (e.g., replayed monitoring logs)
 - *Allows integration with workload generators*
- **Reconfiguration**
 - Triggered by externally provided PCM-specific reconfiguration plans
 - Supports the presented operations
- **Hardware resource scheduling**
 - CPU: Processor-sharing (time slicing)
 - Disk: First-come/first-served
 - *Scheduler interface allows custom disciplines*
- **Measurements**
 - Executions (incl. timing & control-flow information)
 - CPU utilization
 - # Concurrent (system-level) transactions
- **Unsupported PCM features:**
 - (Parametric) stochastic expressions → *planned*
 - Middleware model (as supported by SimuCom)

Initialization of the dynamic simulation model

- 1 store `Components` and their `Services` for lookup
- 2 initialize `AssemblyContexts` (i. e. component instantiation)
- 3 initialize `ResourceContainers` (CPUs, schedulers, ...)
- 4 initialize `AllocationContexts` and deallocate remaining `ResourceContainers`

On occurrence of an entry level system call

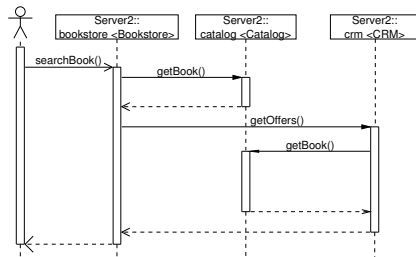
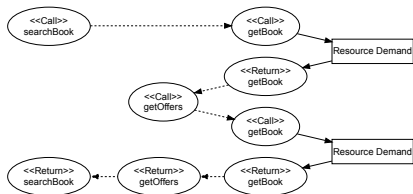
- 1 the corresponding `RDSEFF` is looked up
- 2 the control flow is evaluated (i. e. the `AbstractActions`) and translated into simulatable events
- 3 the first event is scheduled



On occurrence of an entry level system call

- 1 the corresponding RDSEFF is looked up
- 2 the control flow is evaluated (i. e. the AbstractActions) and translated into simulatable events
- 3 the first event is scheduled

Control Flow Events and Sequence Diagram



- 1 Introduction
- 2 SLAstic — Framework & PCM-Specific Reconfiguration
- 3 SLAstic.SIM — Simulator Architecture & Framework Integration
- 4 Evaluation
 - Goals & Methodology
 - Scenario 2 (Varying Workload w/o Reconfiguration)
 - Scenario 3 (Varying Workload w/ Reconfiguration)
- 5 Conclusions

Goals

- Evaluating SLAStic.SIM's performance
- Comparison with SimuCom (performance & results)
- Evaluating effect of reconfiguration on the simulated system

Methodology

- Use of [Bookstore sample application](#) (*PCM instance described in the beginning*)
- **Evaluation scenarios:**
 - 1 [Constant workload](#), comparison with SimuCom [*results in the paper*]
 - 2 [Varying workload intensity](#) w/o reconfiguration
 - 3 [Varying workload intensity](#) w/ [reconfiguration](#)

Hardware and Software Setup

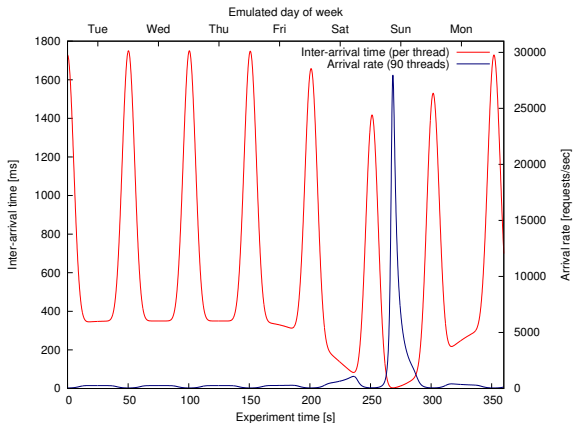
<i>CPU</i>	Intel Core i5, hyper-threading enabled
<i>RAM</i>	4 GB
<i>OS</i>	Ubuntu Generic Linux kernel 2.6.32-22 SMP
<i>Java</i>	Sun Java Version 1.6.0_20
<i>Heap space</i>	1 GB for SLAStic.SIM / 2 GB for SimuCom 3.0

Setup

- Workload generated (offline) by Apache JMeter² + own plug-in³
- Varying inter-arrival times according to an input function
- 68 653 traces
- Simulation time: 360 time units

²<http://jakarta.apache.org/jmeter/> ³<http://code.google.com/p/delayfunction/>

Setup



Varying workload intensity (Scenarios 2 & 3)

Setup

- Workload generated (offline) by Apache JMeter² + own plug-in³
- Varying inter-arrival times according to an input function
- 68 653 traces
- Simulation time: 360 time units

Goal

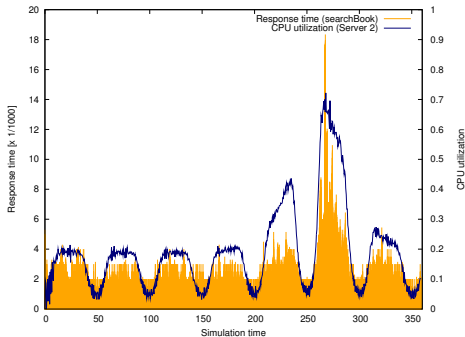
- Demonstrating the performance simulation driven by bursty workload

Results

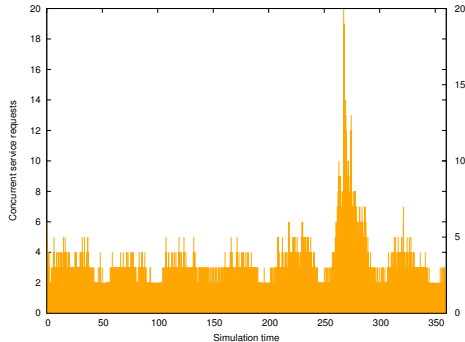
- Simulation took around 18.6 seconds
- Peak in input workload resulted in peaks of both, response times & CPU utilization

²<http://jakarta.apache.org/jmeter/> ³<http://code.google.com/p/delayfunction/>

Results (cont'd)



Response times (searchBook) & CPU utilization



Concurrent transactions

Setup

- Reused varying workload from Scenario 2
- Simulation time: 360 time units
- Reconfigurations at $t = 200$ and $t = 300$
 - $t = 200$: Increase capacity (“weekend plan”)
 - ① Allocation of Server1
 - ② Replicating CRM to Server1
 - ③ Migrating Catalog from Server2 to Server1
 - $t = 300$: Decrease capacity (inverse plan)
 - ① De-replication of CRM from Server1
 - ② Migration of Catalog from Server1 to Server2
 - ③ De-allocation of Server1

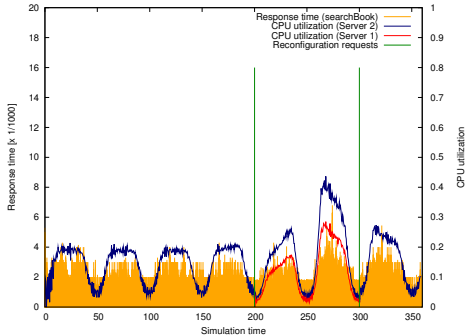
Goal

- Demonstrating the effect of reconfiguration

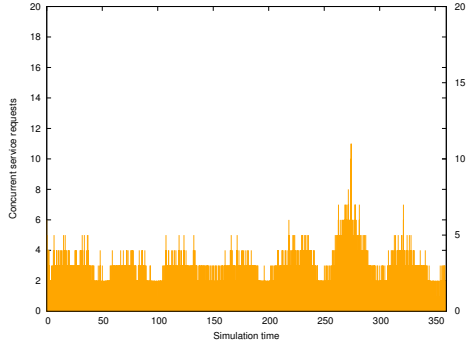
Results

- Simulation took around 18.1 seconds
- Reconfiguring the system lowered the peaks

Results (cont'd)

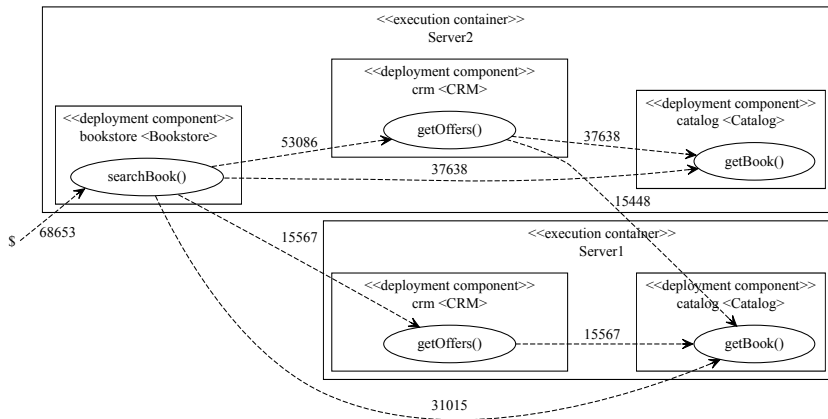


Response times (searchBook) & CPU utilization



Concurrent transactions

Results (cont'd)

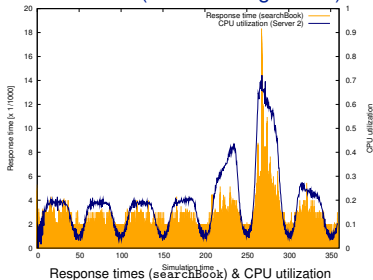


Reconstructed operation dependency graph (deployment view)

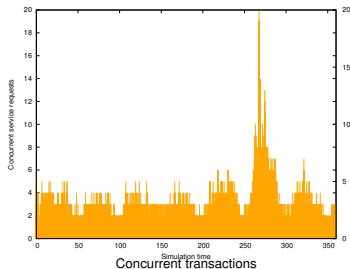
Comparison between Scenarios 1 & 2

Evaluation ▷ Scenario 3 (Varying Workload w/ Reconfiguration)

Scenario 2 (w/o Reconfiguration):

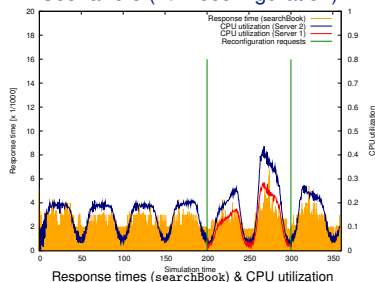


Response times (searchBook) & CPU utilization

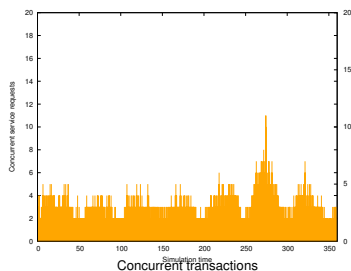


Concurrent transactions

Scenario 3 (w/ Reconfiguration):



Response times (searchBook) & CPU utilization



Concurrent transactions

- Implement **missing PCM features**: Parametric stochastic expressions
- Evaluation with **more complex** ...
 - Models
 - Workloads (from production systems)
- New **features**
 - **Cost model** for reconfigurations (e.g., delays for allocation/replication/...)
 - Additional **strategies for dispatching requests** among replicas
 - Additional **runtime reconfiguration operations**: (e.g., replace implementation [Mat09, Bun08])
 - Additional **QoS properties** (e.g., reliability of execution containers)
- **Dependency injection** (Guice): heavier use & external configuration
- As usual: **Performance** ;-)



Steffen Becker, Heiko Koziolk, and Ralf Reussner.

The Palladio Component Model for model-driven performance prediction.
Journal of Systems and Software, 82(1):3–22, 2009.



Sven Bunge.

Transparentes Redeployment in komponentenbasierten Softwaresystemen ("transparent redeployment in component-based software systems", in German), December 2008.
Diploma Thesis, University of Oldenburg.



Jasminka Matevska.

Architekturbasierte erreichbarkeitsoptimierte Rekonfiguration komponentenbasierter Softwaresysteme zur Laufzeit.
PhD thesis, Department of Computer Science, University of Oldenburg, Oldenburg, Germany, July 2009.



Bernd Page and Wolfgang Kreutzer, editors.

The Java Simulation Handbook: Simulating Discrete Event Systems with UML and Java.
Shaker Verlag, 1. edition, 2005.



Ralf Reussner, Steffen Becker, Jens Happe, Heiko Koziolk, Klaus Krogmann, and Michael Kuperberg.

The Palladio Component Model.
Technical report, Univ. Karlsruhe (TH), June 2007.
http://sdqweb.ipd.uka.de/wiki/Palladio_Component_Model.



André van Hoorn.

Online Capacity Management for Increased Resource Efficiency of Software Systems.
PhD thesis, Dept. Comp. Sc., Univ. Oldenburg, Germany, 2011.
work in progress.



André van Hoorn, Matthias Rohr, Asad Gul, and Wilhelm Hasselbring.

An adaptation framework enabling resource-efficient operation of software systems.
In *Proc. Warm-Up Workshop for ACM/IEEE ICSE 2010 (WUP '09)*, pages 41–44. ACM, April 2009.



André van Hoorn, Matthias Rohr, Wilhelm Hasselbring, Jan Waller, Jens Ehlers, Sören Frey, and Dennis Kieselhorst.

Continuous monitoring of software services: Design and application of the Kieker framework.

Technical Report TR-0921, Dept. Comp. Sc., Univ. Kiel, Germany, November 2009.

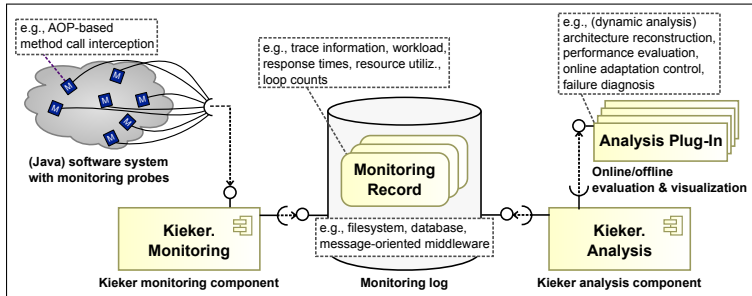
http://www.informatik.uni-kiel.de/uploads/tx_publication/vanhoorn_tr0921.pdf.



Robert von Massow.

Performance simulation of runtime reconfigurable software architectures, April 2010.

Diploma Thesis, Univ. Oldenburg, Germany.



Core Characteristics [vHRH⁺09]

- **Flexible architecture** (custom *probes, readers, writers, analysis plug-ins*)
- **Integrated & extensible record type model** for monitoring & analysis
- Logging, reconstruction, analysis/visualization of **(distributed) traces**
- **Low overhead** (designed for continuous operation in multi-user systems)
- Evaluated in industry case studies

Setup

- Constant workload (log generated by script)
- Open workload with inter-arrival time 0.1
→ yielding 100% simulated CPU utilization
- Simulation time: 1000 time units

Goal

- Comparison of results/performance between SLAStic.SIM & SimuCom

Results (Duration [ms] of 50 simulation runs)

	<i>Min.</i>	<i>Median</i>	<i>Mean</i>	<i>Max.</i>	<i>Dev.</i>
SimuCom	6434	7179	7199	7873	287.24
SLAStic.SIM	4864	5325	5333	5833	161.25

(Duration excl. code generation (SimuCom) & initialization (SLAStic.SIM))